



INFORMATION DISCLOSURE CITATION
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ATTY DOCKET NO.

ITO.0045US (P16093)

SERIAL NO.

10/634,130

APPLICANT(S):

Brian G. Johnson et al.

FILING DATE:

August 4, 2003

GROUP ART UNIT:

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
TL	A.	4,115,872	09/19/1978	Bluhm			
	B.	4,366,614	01/04/1983	Kumurdjian			
	C.	4,177,475	12/04/1979	Holmberg			
	D.	5,789,758	08/04/1998	Reinberg			
	E.						

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TL	F.	2003/0001230	01/02/2003	Lowrey			
TL	G.	2003/0116794	06/26/2003	Lowrey			
	H.						
	I.						
	J.						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
TL	K.	WO 03/023875	03/20/2003	PCT				
	L.							
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	N.							
	O.							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	P.	
	Q.	
	R.	
	S.	
	T.	
	U.	
	V.	

EXAMINER

DATE CONSIDERED

12/23/05

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FILING DATE:

August 4, 2003

GROUP ART UNIT:

2651

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A.						
	B.						
	C.						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	D.							
	E.							
	F.							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>TL</i>	G.	Hwang, Y.N., Hong, J.S., Lee, S.H., Ahn, S.J., Jeong, G.T., Koh, G.H., Kim, H.J., Jeong, W.C., Lee, S.Y., Park, J.H., Ryoo, K.C., Horii, H., Ha, Y.H., Yi, J.H., Cho, W.Y., Kim, Y.T., Lee, K.H., Joo, S.H., Park, S.O., Jeong, U.I., Jeong, H.S. and Kim, Kinam, "Completely CMOS-Compatible Phase-Change Nonvolatile RAM Using NMOS Cell Transistors," presented at 2003 19 th IEEE Non-Volatile Semiconductor Memory Workshop, Monterey, California, February 26-20, 2003
<i>TL</i>	H.	Ha, Y.H., Yi, J.H., Horii, H., Park, J.H., Joo, S.H., Park, S.O., Chung, U-In and Moon, J.T., "An Edge Contact Type Cell for Phase Change RAM Featuring Very Low Power Consumption," presented at IEEE 2003 Symposium on VLSI Technology, Kyoto, Japan, June 12-14, 2003
<i>TL</i>	I.	Hwang, Y.N., Hong, J.S., Lee, S.H., Ahn, S.J., Jeong, G.T., Koh, G.H., Oh, J.H., Kim, H.J., Jeong, W.C., Lee, S.Y., Park, J.H., Ryoo, K.C., Horii, H., Ha, Y.H., Yi, J.H., Cho, W.Y., Kim, Y.T., Lee, K.H., Joo, S.H., Park, S.O., Chung, U.I., Jeong, H.S. and Kim, Kinam, "Full Integration and Reliability Evaluation of Phase-change RAM Based on 0.24 mm-CMOS Technologies," presented at IEEE 2003 Symposium on VLSI Technology, Kyoto, Japan, June 12-14, 2003
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<i>TL</i>	L.	Lowrey, T.A., "Memory and Access Device and Method Therefor," U.S. Patent Application Serial No. 10/319,764, filed December 13, 2002
	M.	

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